orney Docket No.	214391US2S

First Inventor or Application Identifier Hidetaka HA

POWER SEMICONDUCTOR DEVICE THEREOF

MANUFACTURING METHOD

PTC

Assignee Name: Assignee Address: S. P.

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	Assistant Commissioner for Patents Box Patent Application Washington, DC 20231			
Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS Assignment Papers (cover sheet & document(s))			
2. Specification Total Sheets 34	8. Application Data Sheet. See 37 CFR 1.76 9. 37 C.F.R. §3.73(b) Statement Power of Attorney			
3. ☐ Formal Drawing(s) Total Sheets 6	10. ☐ English Translation Document (if applicable)			
4. Oath or Declaration	11. □ Information Disclosure			
17: If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below:				
Continuation Divisional Continuation-in-part (CIP) of prior application no.: ###Prior application information: Examiner: Group Art Unit: ###FOIL CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.				
18. Amend the specification by inserting before the first line the sentence: □ This application is a □ Continuation □ Division □ Continuation-in-part (CIP) of application Serial No. Filed on □ Which was published in English □ Which was not published in English □ This application claims priority of provisional application Serial No. Filed				
19. CORRESPONDENCE ADDRESS				

Name: Marvin J. Spivak

Signature: Date: 9/25/b)

Name: C. Irvin McClelland Registration No.:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Hidetaka HATTORI **New Application**

SERIAL NO:

FfLING DATE: Herewith

FOR:

POWER SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	11 - 20 =	0	× \$18 =	\$0.00
INDEPENDENT CLAIMS	2 - 3 =	0	× \$80 =	\$0.00
□ MULTIPLE DEPENDENT CLAIMS (If applicable) + \$270 =			\$0.00	
□ LATE FILING OF DECLARATION + \$130 =			\$0.00	
BASIC FEE				\$710.00
TOTAL OF ABOVE CALCULATIONS			\$710.00	
□ REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
□ FILING IN NON-ENGLISH LANGUAGE		+ \$130 =	\$0.00	
☐ RECORDATION OF ASSIGNMENT + \$40 =		+ \$40 =	\$40.00	
			TOTAL	\$750.00

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The Commissioner is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Date:

A STATE Ħ

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